Notice of References Cited	Application/Control No. 10/680,158	Applicant(s)/Patent Under Reexamination MOULI, CHANDRA	
	Examiner Howard Weiss	Art Unit 2814	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,838,322	01-2005	Pham et al.	438/151
	B	US-6,806,537	10-2004	Matsumoto et al.	257/347
	C	US-6,221,724	04-2001	Yu et al.	438/289
	D	US-5,448,513	09-1995	Hu et al.	365/150
	E	US-5,307,305	04-1994	Takasu	365/145
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Ohsawa, T., et al., "Memory Design Using a One-Transistor Gain Cell on SOI", Nov. 2002, IEEE J. Solid-State Circuits, Vol. 37, No. 11, pp. 1510-22
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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